

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 09/476,622 CHIN ET AL.	
		Examiner	Art Unit	Page 1 of 1 William M. Treat 2183

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Margulis, i860 Microprocessor Architecture, Intel/Osborne McGraw-Hill, Berkeley, 1990, pp. 79-94 and pp. 139-140.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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